

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant : Yasuo Matsushita  
Appl. No. : --  
Filed : Concurrently herewith  
For : A CONNECTOR AND A CONNECTOR ASSEMBLY

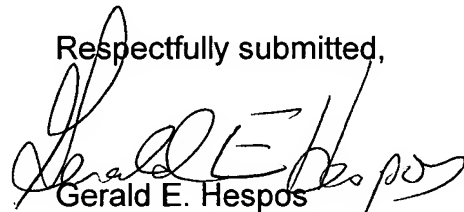
MS Patent Application  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT**

Sir:

This Information Disclosure Statement includes a copy of Form PTO-1449 that identifies one Japanese reference and two U.S. patents. A copy of the Japanese reference is attached as well. The relevancy of the Japanese reference is described in paragraphs 0002 and 0003 of the subject application. The two U.S. patents relate to the same general technology, namely panel-mounted connectors and grommets for such connectors. The Examiner is requested to consider these references during the examination and to make the references of record.

Respectfully submitted,



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Date: November 19, 2003

# INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

Docket Number (Optional)

FP03-159US

Application Number

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Applicant(s)

Yasuo Matsushita

Filing Date

Concurrently herewith

Group Art Unit

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## U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
		5,487,680	1-1996	Yamanashi			
		5,775,944	7-1998	Flask et al.			

## FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO
		7-53269	1995	Japan				✓

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)


EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.